

**IN THE CLAIMS**

1. (Original) A semiconductor integrated circuit device including

a plurality of internal circuits for processing signals individually; and

a plurality of signal lines connected individually with said internal circuits, a first voltage line for feeding a first voltage to said internal circuits, and a second voltage line for feeding a second voltage to said internal circuits, comprising:

a first protective circuit connected between the signal lines and said first voltage line of each of said internal circuits for allowing an electric current to flow from said first voltage line to said signal lines at a protection time against an electrostatic breakdown;

a second protective circuit connected between the signal lines and said second voltage line of each of said internal circuits allowing an electric current to flow from said signal lines to said second voltage line at a protection time against an electrostatic breakdown;

a third protective circuit connected between the signal lines and said first voltage line of each of said internal circuits for allowing an electric current to flow from said signal lines to said first voltage line at a protection time against an electrostatic breakdown; and

a fourth protective circuit connected between the signal lines and said second voltage line of each of said internal circuits for allowing an electric current to flow from said second voltage line to said signal lines at a protection time against an electrostatic breakdown.

Claims 2-11 (Canceled)